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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*  
 This application is a 371 of PCT/FR00/00559 03/07/2000 *Not Applicable DG*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*  
 FRANCE 99/02823 03/08/1999 *Not Applicable DG*

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after <i>allowance</i> <i>DG</i> Examiner's Signature Initials	STATE OR COUNTRY FRANCE	SHEETS DRAWING 3	TOTAL CLAIMS 27	INDEPENDENT CLAIMS 3
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TITLE

Method for testing integrated circuits with memory element access

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